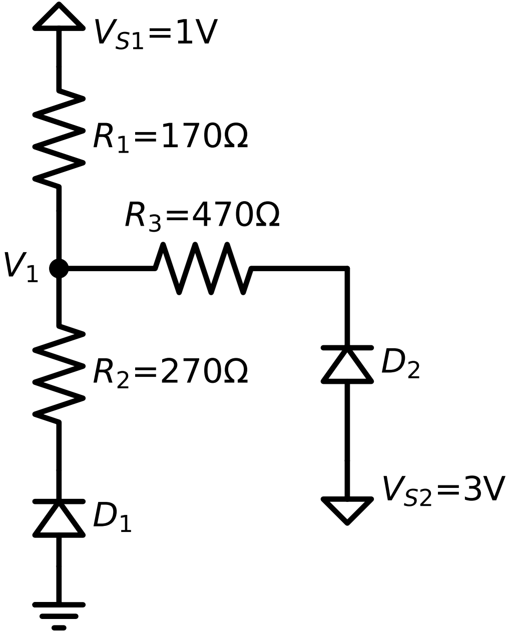


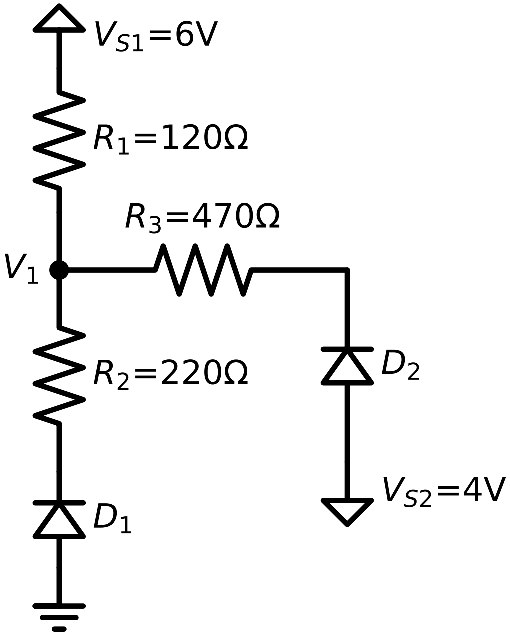
(a)

case1: FB, FB: V_1=0.699
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-5.181mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=3.406mA >0, True

case2: FB, RB: V_1=0.343
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-3.863mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=2.657V <=0.7, False

case3: RB, FB: V_1=1.345
D_1:RB, I_{D1}=0V (open), V_{D1}=-1.345V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=2.032mA >0, True

case4: RB, RB: V_1=1.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-1.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=2.0V <=0.7, False



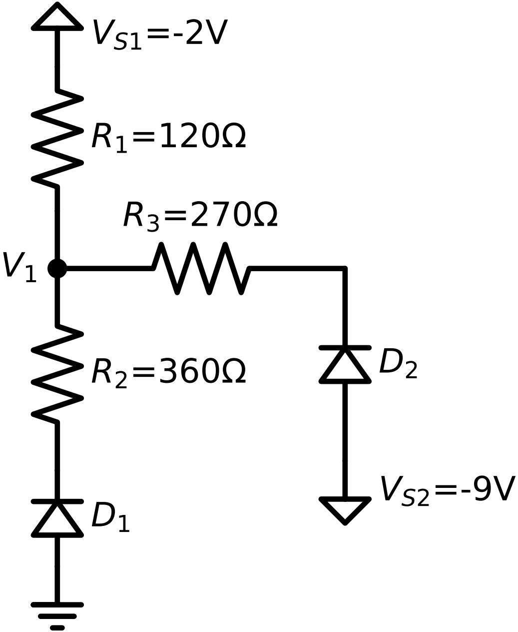
(b)

case1: FB, FB: V_1=3.588
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-19.491mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-0.613mA >0, False

case2: FB, RB: V_1=3.635
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-19.705mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=0.365V <=0.7, True

case3: RB, FB: V_1=5.451
D_1:RB, I_{D1}=0V (open), V_{D1}=-5.451V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-4.577mA >0, False

case4: RB, RB: V_1=6.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-6.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-2.0V <=0.7, True



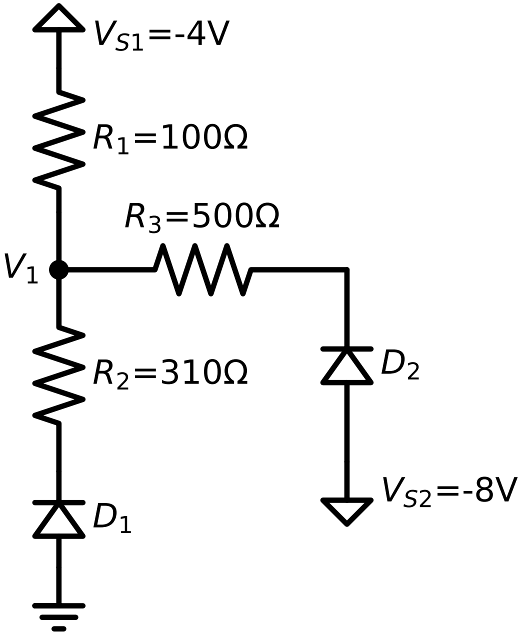
(c)

case1: FB, FB: V_1=-3.681
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=8.281mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-22.293mA >0, False

case2: FB, RB: V_1=-1.675
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=2.708mA >0, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-7.325V <=0.7, True

case3: RB, FB: V_1=-4.369
D_1:RB, I_{D1}=0V (open), V_{D1}=4.369V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-19.744mA >0, False

case4: RB, RB: V_1=-2.0
D_1:RB, I_{D1}=0V (open), V_{D1}=2.0V <=0.7, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-7.0V <=0.7, True



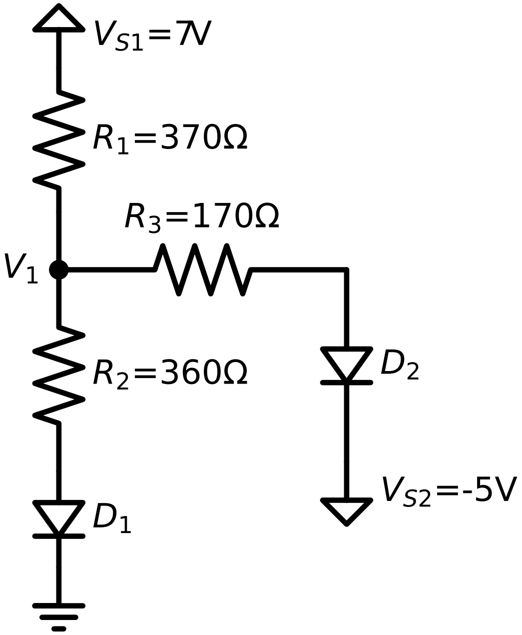
(d)

case1: FB, FB: V_1=-3.918
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=10.381mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-9.564mA >0, False

case2: FB, RB: V_1=-3.195
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=8.048mA >0, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-4.805V <=0.7, True

case3: RB, FB: V_1=-4.783
D_1:RB, I_{D1}=0V (open), V_{D1}=4.783V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-7.834mA >0, False

case4: RB, RB: V_1=-4.0
D_1:RB, I_{D1}=0V (open), V_{D1}=4.0V <=0.7, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-4.0V <=0.7, True



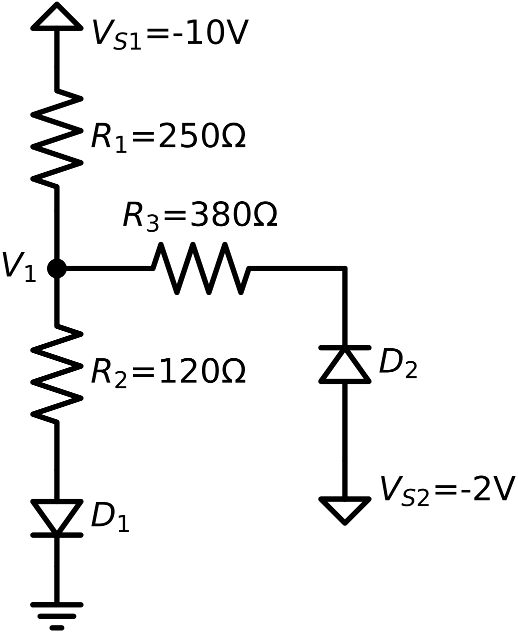
(e)

case1: FB, FB: V_1=-0.39
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-3.028mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=23.0mA >0, True

case2: FB, RB: V_1=3.807
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=8.631mA >0, True
D_2:RB, I_{D2}=0V (open), V_{D2}=8.807V <=0.7, False

case3: RB, FB: V_1=-0.743
D_1:RB, I_{D1}=0V (open), V_{D1}=-0.743V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=20.924mA >0, True

case4: RB, RB: V_1=7.0
D_1:RB, I_{D1}=0V (open), V_{D1}=7.0V <=0.7, False
D_2:RB, I_{D2}=0V (open), V_{D2}=12.0V <=0.7, False



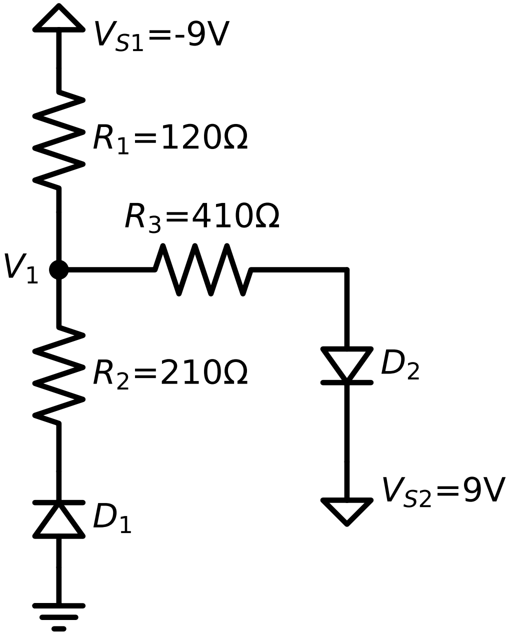
(f)

case1: FB, FB: V_1=-2.758
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-28.817mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=0.153mA >0, True

case2: FB, RB: V_1=-2.77
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-28.917mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=0.77V <=0.7, False

case3: RB, FB: V_1=-7.103
D_1:RB, I_{D1}=0V (open), V_{D1}=-7.103V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=11.587mA >0, True

case4: RB, RB: V_1=-10.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-10.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=8.0V <=0.7, False



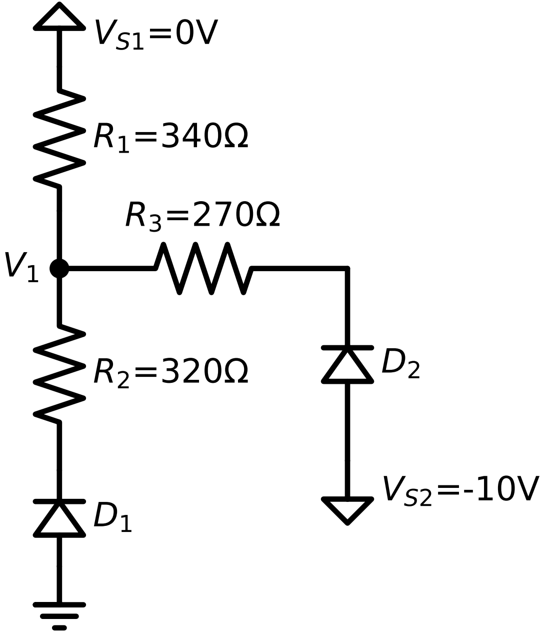
(g)

case1: FB, FB: V_1=-3.52
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=13.429mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-32.244mA >0, False

case2: FB, RB: V_1=-5.982
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=25.152mA >0, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-14.982V <=0.7, True

case3: RB, FB: V_1=-4.766
D_1:RB, I_{D1}=0V (open), V_{D1}=4.766V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-35.283mA >0, False

case4: RB, RB: V_1=-9.0
D_1:RB, I_{D1}=0V (open), V_{D1}=9.0V <=0.7, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-18.0V <=0.7, True



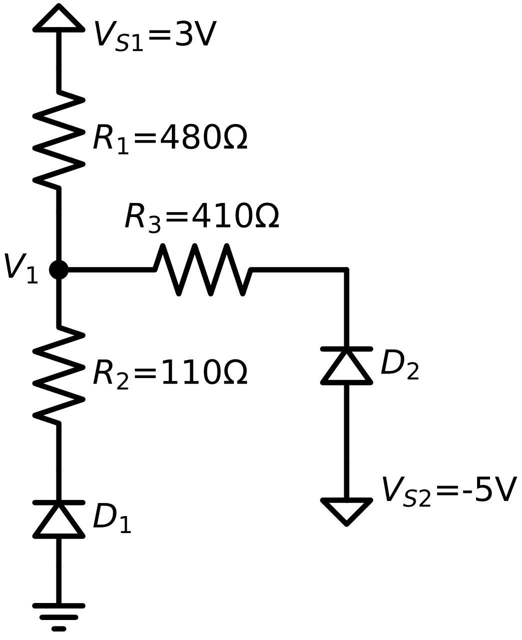
(h)

case1: FB, FB: V_1=-4.28
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=11.188mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-23.778mA >0, False

case2: FB, RB: V_1=-0.361
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-1.059mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-9.639V <=0.7, True

case3: RB, FB: V_1=-5.964
D_1:RB, I_{D1}=0V (open), V_{D1}=5.964V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-17.541mA >0, False

case4: RB, RB: V_1=-0.0
D_1:RB, I_{D1}=0V (open), V_{D1}=0.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-10.0V <=0.7, True



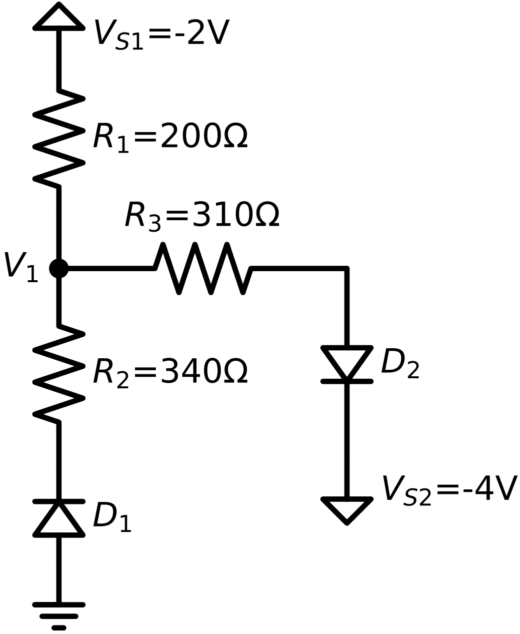
(i)

case1: FB, FB: V_1=-1.03
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=3.0mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-11.39mA >0, False

case2: FB, RB: V_1=-0.01
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-6.273mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-4.99V <=0.7, True

case3: RB, FB: V_1=-1.692
D_1:RB, I_{D1}=0V (open), V_{D1}=1.692V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-9.776mA >0, False

case4: RB, RB: V_1=3.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-3.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-8.0V <=0.7, True



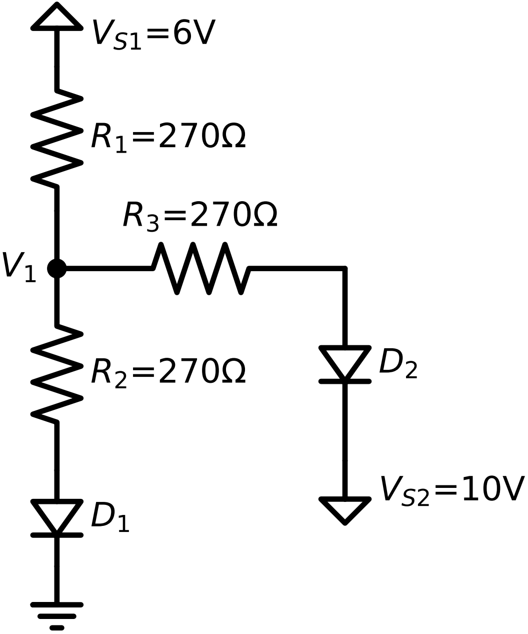
(j)

case1: FB, FB: V_1=-2.033
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=3.921mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=4.087mA >0, True

case2: FB, RB: V_1=-1.519
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=2.409mA >0, True
D_2:RB, I_{D2}=0V (open), V_{D2}=2.481V <=0.7, False

case3: RB, FB: V_1=-2.51
D_1:RB, I_{D1}=0V (open), V_{D1}=2.51V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=2.548mA >0, True

case4: RB, RB: V_1=-2.0
D_1:RB, I_{D1}=0V (open), V_{D1}=2.0V <=0.7, False
D_2:RB, I_{D2}=0V (open), V_{D2}=2.0V <=0.7, False



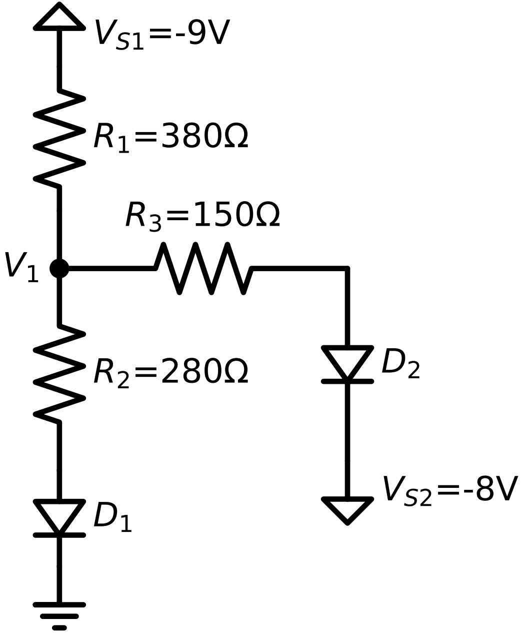
(k)

case1: FB, FB: V_1=5.8
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=18.889mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-18.148mA >0, False

case2: FB, RB: V_1=3.35
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=9.815mA >0, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-6.65V <=0.7, True

case3: RB, FB: V_1=8.35
D_1:RB, I_{D1}=0V (open), V_{D1}=8.35V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-8.704mA >0, False

case4: RB, RB: V_1=6.0
D_1:RB, I_{D1}=0V (open), V_{D1}=6.0V <=0.7, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-4.0V <=0.7, True



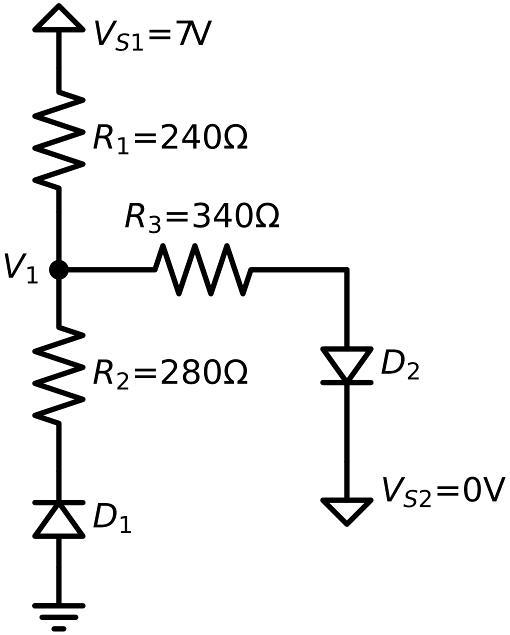
(I)

case1: FB, FB: V_1=-5.428
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-21.886mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=12.48mA >0, True

case2: FB, RB: V_1=-3.415
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-14.696mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=4.585V <=0.7, False

case3: RB, FB: V_1=-7.781
D_1:RB, I_{D1}=0V (open), V_{D1}=-7.781V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-3.207mA >0, False

case4: RB, RB: V_1=-9.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-9.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-1.0V <=0.7, True



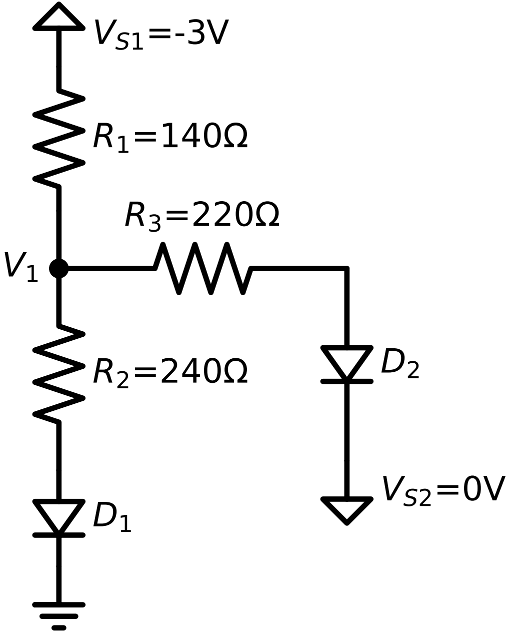
(m)

case1: FB, FB: V_1=2.69
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-12.107mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=5.853mA >0, True

case2: FB, RB: V_1=3.446
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-14.807mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=3.446V <=0.7, False

case3: RB, FB: V_1=4.393
D_1:RB, I_{D1}=0V (open), V_{D1}=-4.393V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=10.862mA >0, True

case4: RB, RB: V_1=7.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-7.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=7.0V <=0.7, False



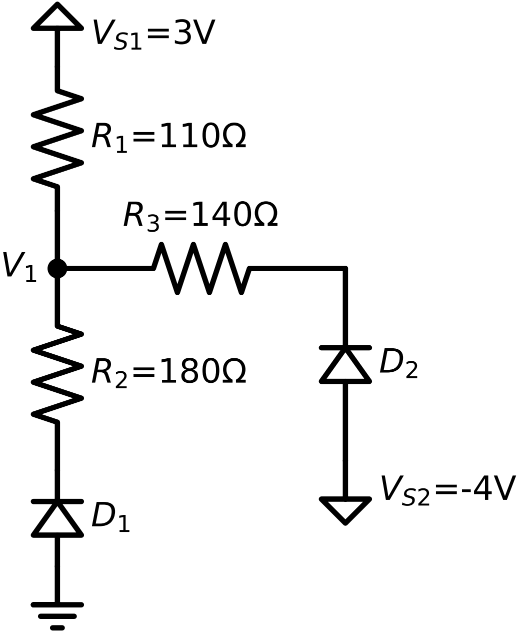
(n)

case1: FB, FB: V_1=-0.967
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-6.946mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-7.577mA >0, False

case2: FB, RB: V_1=-1.637
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-9.737mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-1.637V <=0.7, True

case3: RB, FB: V_1=-1.561
D_1:RB, I_{D1}=0V (open), V_{D1}=-1.561V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-10.277mA >0, False

case4: RB, RB: V_1=-3.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-3.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-3.0V <=0.7, True



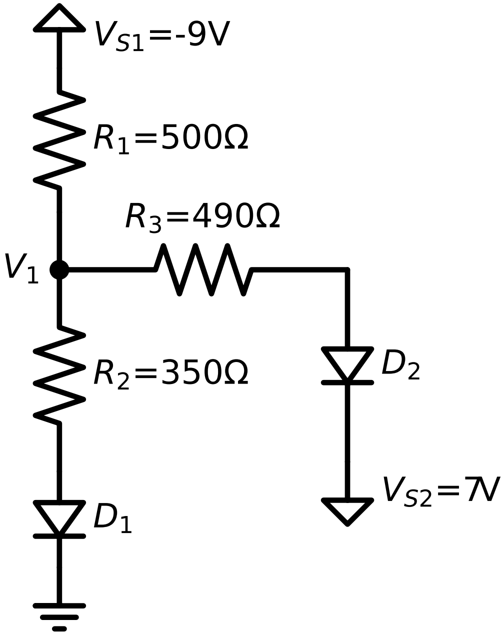
(o)

case1: FB, FB: V_1=-0.468
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-1.289mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-30.229mA >0, False

case2: FB, RB: V_1=1.597
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-12.761mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-5.597V <=0.7, True

case3: RB, FB: V_1=-0.388
D_1:RB, I_{D1}=0V (open), V_{D1}=0.388V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-30.8mA >0, False

case4: RB, RB: V_1=3.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-3.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-7.0V <=0.7, True



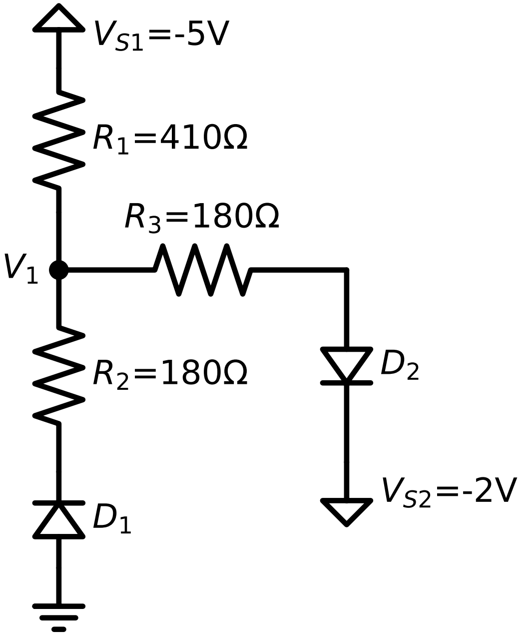
(p)

case1: FB, FB: V_1=-0.041
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-2.117mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-15.798mA >0, False

case2: FB, RB: V_1=-3.294
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-11.411mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-10.294V <=0.7, True

case3: RB, FB: V_1=-0.566
D_1:RB, I_{D1}=0V (open), V_{D1}=-0.566V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-16.869mA >0, False

case4: RB, RB: V_1=-9.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-9.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-16.0V <=0.7, True



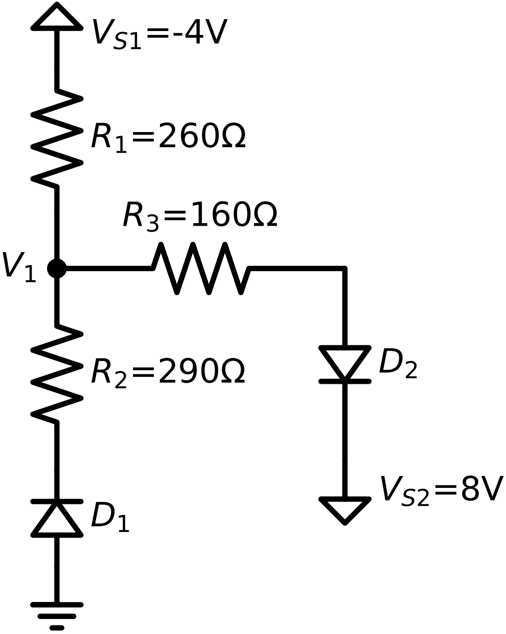
(q)

case1: FB, FB: V_1=-1.72
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=5.667mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-2.333mA >0, False

case2: FB, RB: V_1=-2.012
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=7.289mA >0, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-0.012V <=0.7, True

case3: RB, FB: V_1=-2.429
D_1:RB, I_{D1}=0V (open), V_{D1}=2.429V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-6.272mA >0, False

case4: RB, RB: V_1=-5.0
D_1:RB, I_{D1}=0V (open), V_{D1}=5.0V <=0.7, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-3.0V <=0.7, True



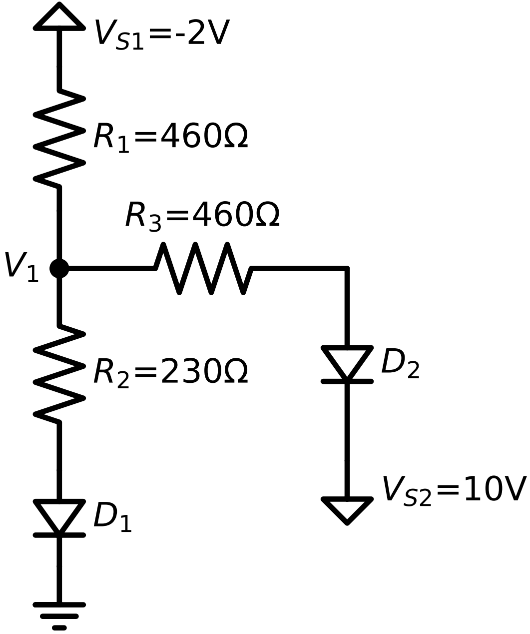
(r)

case1: FB, FB: V_1=2.7
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-11.724mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-37.5mA >0, False

case2: FB, RB: V_1=-2.44
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=6.0mA >0, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-10.44V <=0.7, True

case3: RB, FB: V_1=3.862
D_1:RB, I_{D1}=0V (open), V_{D1}=-3.862V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-30.237mA >0, False

case4: RB, RB: V_1=-4.0
D_1:RB, I_{D1}=0V (open), V_{D1}=4.0V <=0.7, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-12.0V <=0.7, True



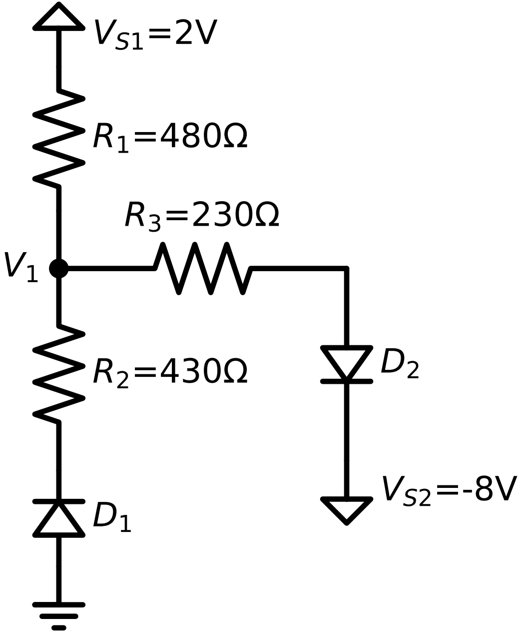
(s)

case1: FB, FB: V_1=2.525
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=7.935mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-17.772mA >0, False

case2: FB, RB: V_1=-0.2
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-3.913mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-10.2V <=0.7, True

case3: RB, FB: V_1=4.35
D_1:RB, I_{D1}=0V (open), V_{D1}=4.35V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-13.804mA >0, False

case4: RB, RB: V_1=-2.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-2.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-12.0V <=0.7, True



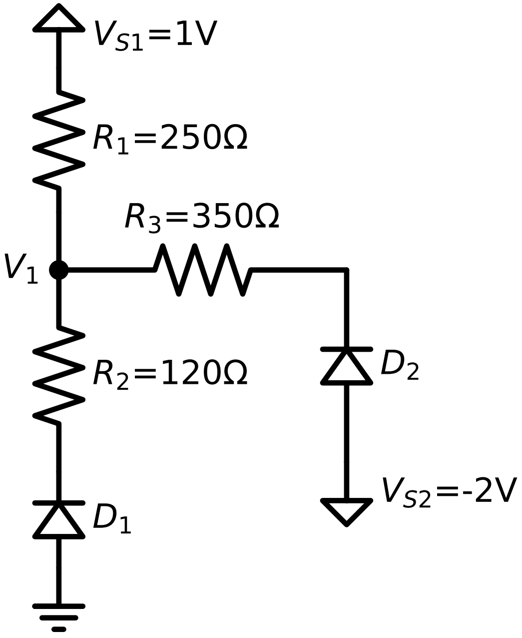
(t)

case1: FB, FB: V_1=-3.335
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=6.128mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=17.239mA >0, True

case2: FB, RB: V_1=0.576
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-2.967mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=8.576V <=0.7, False

case3: RB, FB: V_1=-4.287
D_1:RB, I_{D1}=0V (open), V_{D1}=4.287V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=13.1mA >0, True

case4: RB, RB: V_1=2.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-2.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=10.0V <=0.7, False



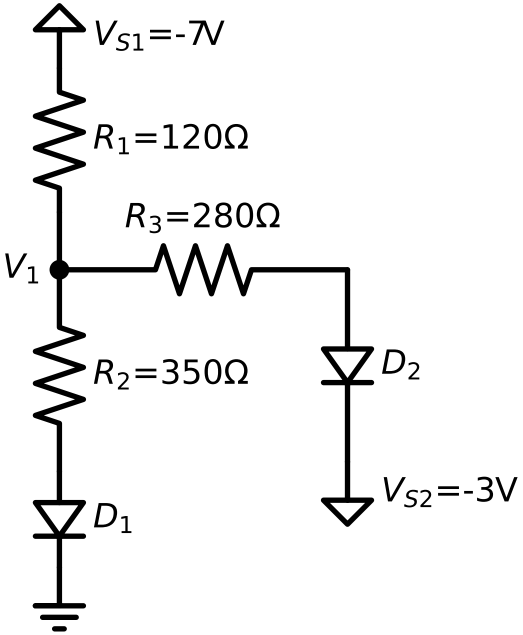
(u)

case1: FB, FB: V_1=-0.629
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-0.592mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-5.917mA >0, False

case2: FB, RB: V_1=-0.149
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-4.592mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-1.851V <=0.7, True

case3: RB, FB: V_1=-0.542
D_1:RB, I_{D1}=0V (open), V_{D1}=0.542V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-6.166mA >0, False

case4: RB, RB: V_1=1.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-1.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-3.0V <=0.7, True



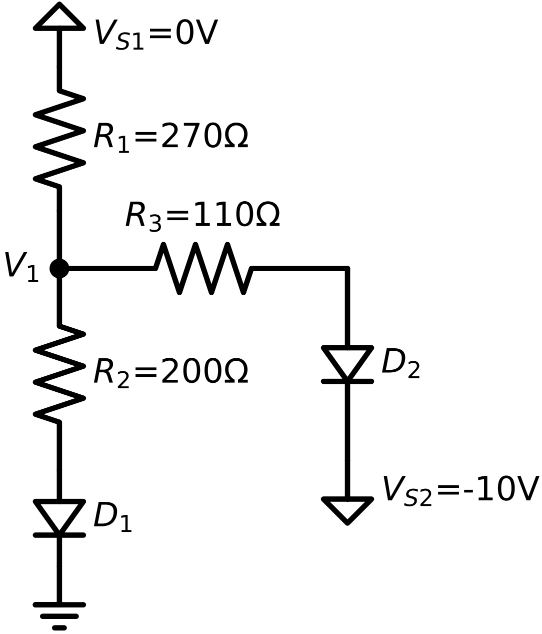
(v)

case1: FB, FB: V_1=-4.373
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-14.494mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-7.404mA >0, False

case2: FB, RB: V_1=-5.034
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-16.383mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-2.034V <=0.7, True

case3: RB, FB: V_1=-5.59
D_1:RB, I_{D1}=0V (open), V_{D1}=-5.59V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-11.75mA >0, False

case4: RB, RB: V_1=-7.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-7.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-4.0V <=0.7, True



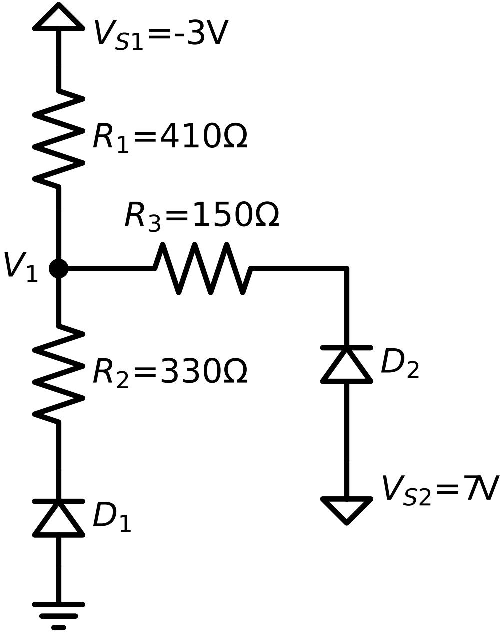
(w)

case1: FB, FB: V_1=-4.554
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-26.27mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=43.145mA >0, True

case2: FB, RB: V_1=0.402
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-1.49mA >0, False
D_2:RB, I_{D2}=0V (open), V_{D2}=10.402V <=0.7, False

case3: RB, FB: V_1=-6.608
D_1:RB, I_{D1}=0V (open), V_{D1}=-6.608V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=24.473mA >0, True

case4: RB, RB: V_1=-0.0
D_1:RB, I_{D1}=0V (open), V_{D1}=-0.0V <=0.7, True
D_2:RB, I_{D2}=0V (open), V_{D2}=10.0V <=0.7, False



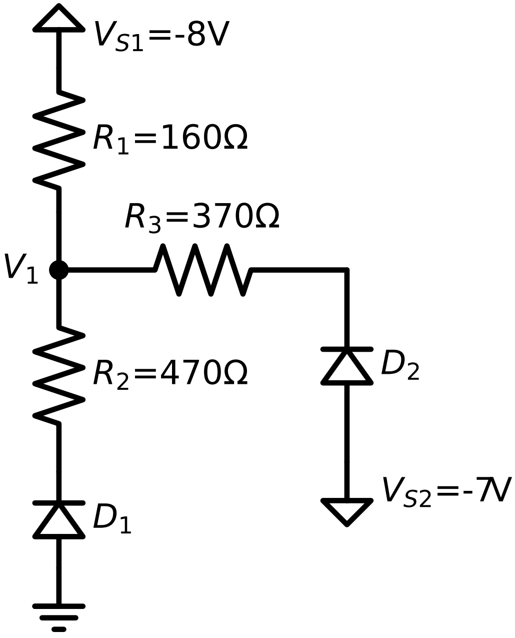
(x)

case1: FB, FB: V_1=2.683
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=-10.252mA >0, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=24.113mA >0, True

case2: FB, RB: V_1=-1.726
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=3.109mA >0, True
D_2:RB, I_{D2}=0V (open), V_{D2}=8.726V <=0.7, False

case3: RB, FB: V_1=3.809
D_1:RB, I_{D1}=0V (open), V_{D1}=-3.809V <=0.7, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=16.607mA >0, True

case4: RB, RB: V_1=-3.0
D_1:RB, I_{D1}=0V (open), V_{D1}=3.0V <=0.7, False
D_2:RB, I_{D2}=0V (open), V_{D2}=10.0V <=0.7, False



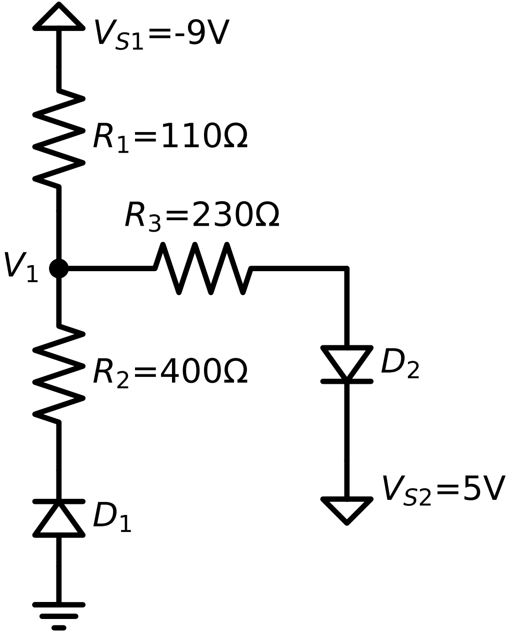
(y)

case1: FB, FB: V_1=-6.525
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=12.394mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-3.176mA >0, False

case2: FB, RB: V_1=-6.146
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=11.587mA >0, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-0.854V <=0.7, True

case3: RB, FB: V_1=-7.909
D_1:RB, I_{D1}=0V (open), V_{D1}=7.909V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=0.565mA >0, True

case4: RB, RB: V_1=-8.0
D_1:RB, I_{D1}=0V (open), V_{D1}=8.0V <=0.7, False
D_2:RB, I_{D2}=0V (open), V_{D2}=1.0V <=0.7, False



(z)

case1: FB, FB: V_1=-3.688
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=7.47mA >0, True
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-40.817mA >0, False

case2: FB, RB: V_1=-7.21
D_1:FB, V_{D1}=0.7V (CVD), I_{D1}=16.275mA >0, True
D_2:RB, I_{D2}=0V (open), V_{D2}=-12.21V <=0.7, True

case3: RB, FB: V_1=-4.244
D_1:RB, I_{D1}=0V (open), V_{D1}=4.244V <=0.7, False
D_2:FB, V_{D2}=0.7V (CVD), I_{D2}=-43.235mA >0, False

case4: RB, RB: V_1=-9.0
D_1:RB, I_{D1}=0V (open), V_{D1}=9.0V <=0.7, False
D_2:RB, I_{D2}=0V (open), V_{D2}=-14.0V <=0.7, True