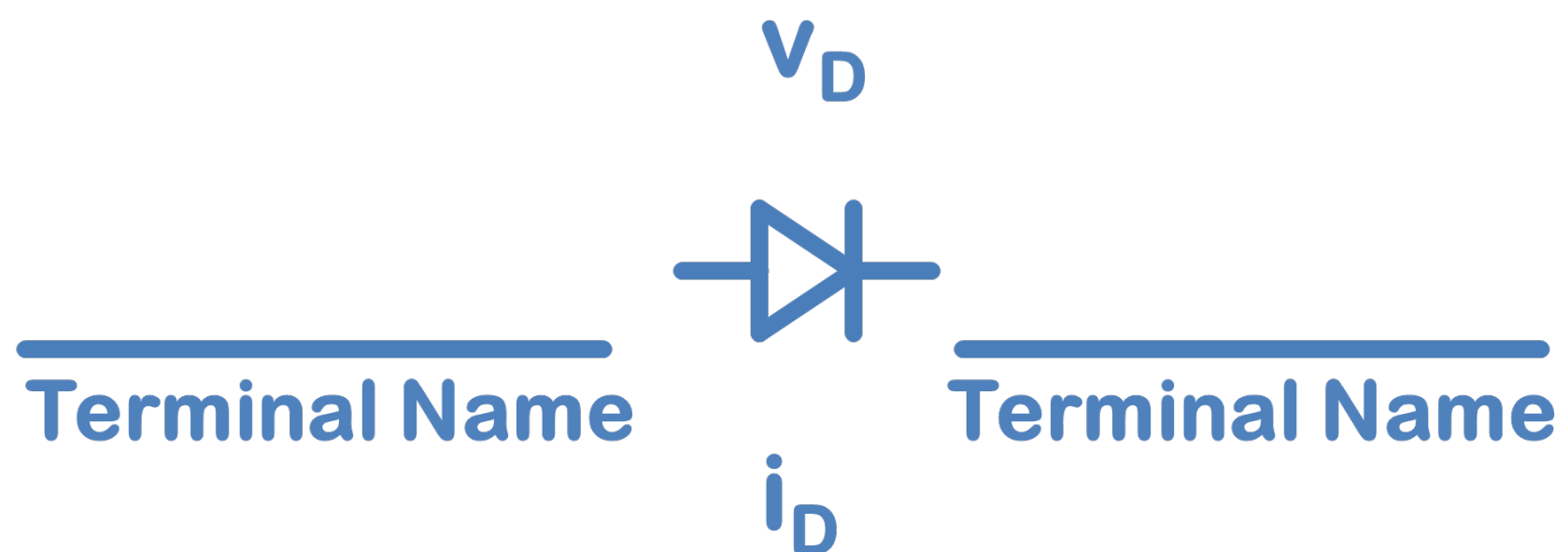


EECS312 Quiz 1

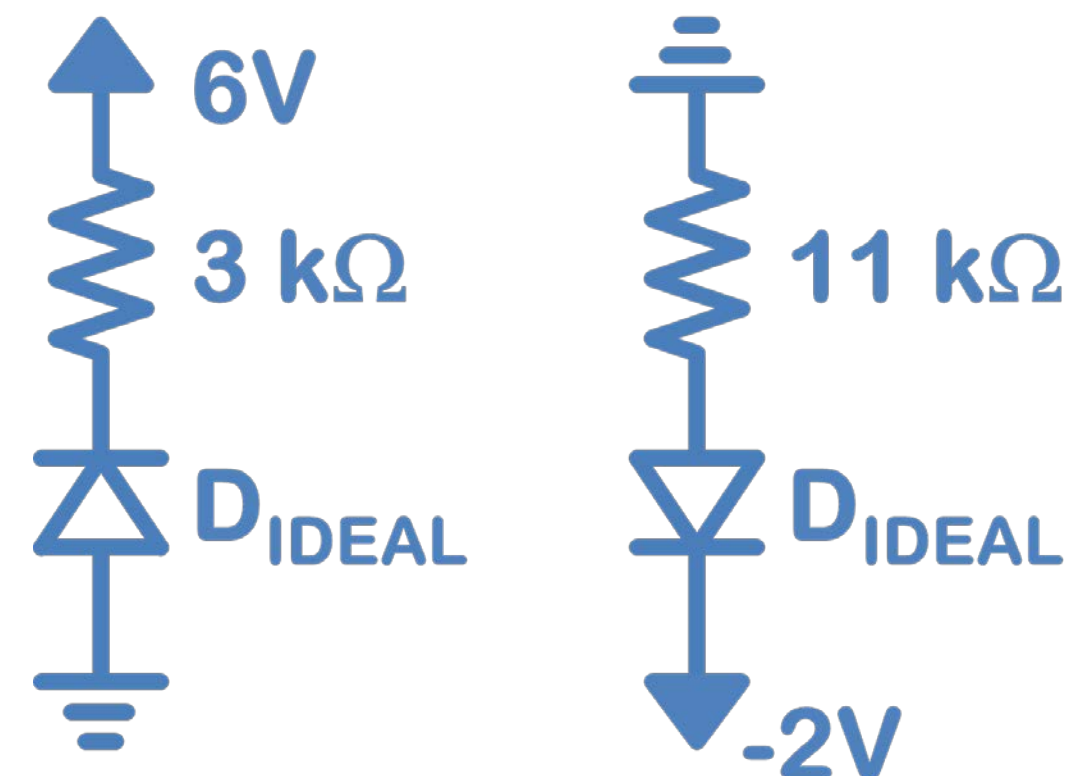
a) Fill in the Table below

BIAS	Equality Variable	Equality Condition	Equivalent Circuit	Inequality Variable	Inequality Condition
Forward Bias (FB)					
Reverse Bias (RB)					

b) label positive current and voltage directions and fill in the terminal names.



c) Find i_D and v_D for the two circuits below.

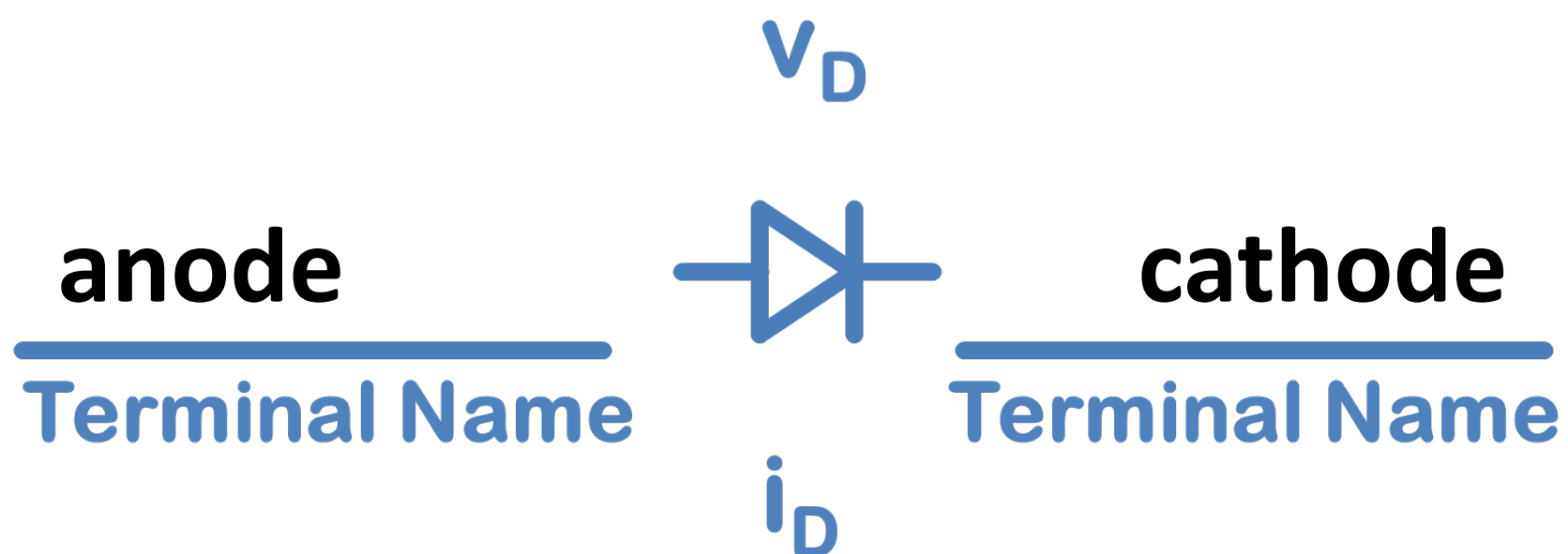


EECS312 Quiz 1

a) Fill in the Table below

BIAS	Equality Variable	Equality Condition	Equivalent Circuit	Inequality Variable	Inequality Condition
Forward Bias (FB)	v_D	$v_D=0$	short	i_D	$i_D>0$
Reverse Bias (RB)	i_D	$i_D=0$	open	v_D	$v_D<0$

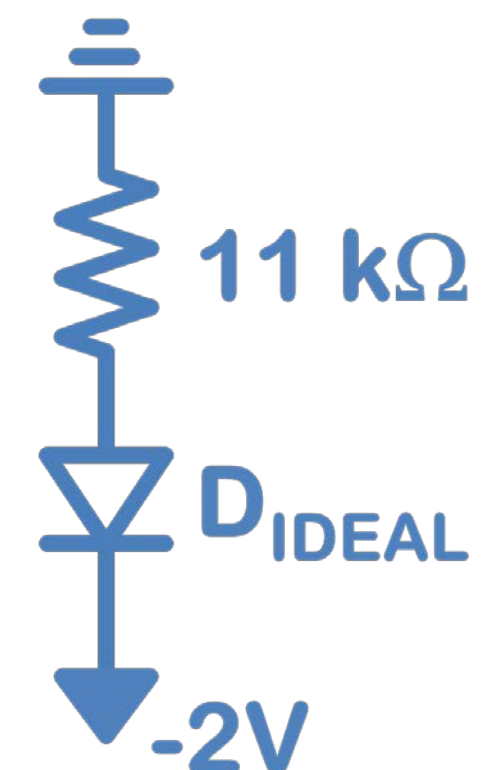
b) label positive current and voltage directions and fill in the terminal names.



c) Find i_D and v_D for the two circuits below.



RB:
 $i_D=0$
 $v_D=-6\text{V}$



FB:
 $v_D=0$
 $i_D=(2/11)\text{mA}$