LAB REPORT OF (name of the experiment)

OBJECTIVE:
The operation you wish to perform in the experiment.

Example (if you wish to implement half adder):
To design, implement and demonstrate a half adder using Xilinx Vivado and Nexys 4 FPGA board.

CODE:
You need to put your VHDL code here for the experiment you performed.

OPERATION:
You must give a brief but detailed description about the operation of your circuit. You must explain in your own words, everything you understand of the circuit. (Schematic capture of the circuit is encouraged)

Example (if you wish to implement half adder):
The main operation of this circuit is to add two bits. The output is high when only one input is high, which is an XOR operation. We have our carry output high only when both our inputs are high which is the logical AND operation.

RESULT:
The result you achieved. Also, if you have performed your simulation, your simulation results should be pasted here (picture).

Example (if you wish to implement half adder):
Thus we have successfully implemented the half adder on Nexys 4 board using Xilinx Vivado.